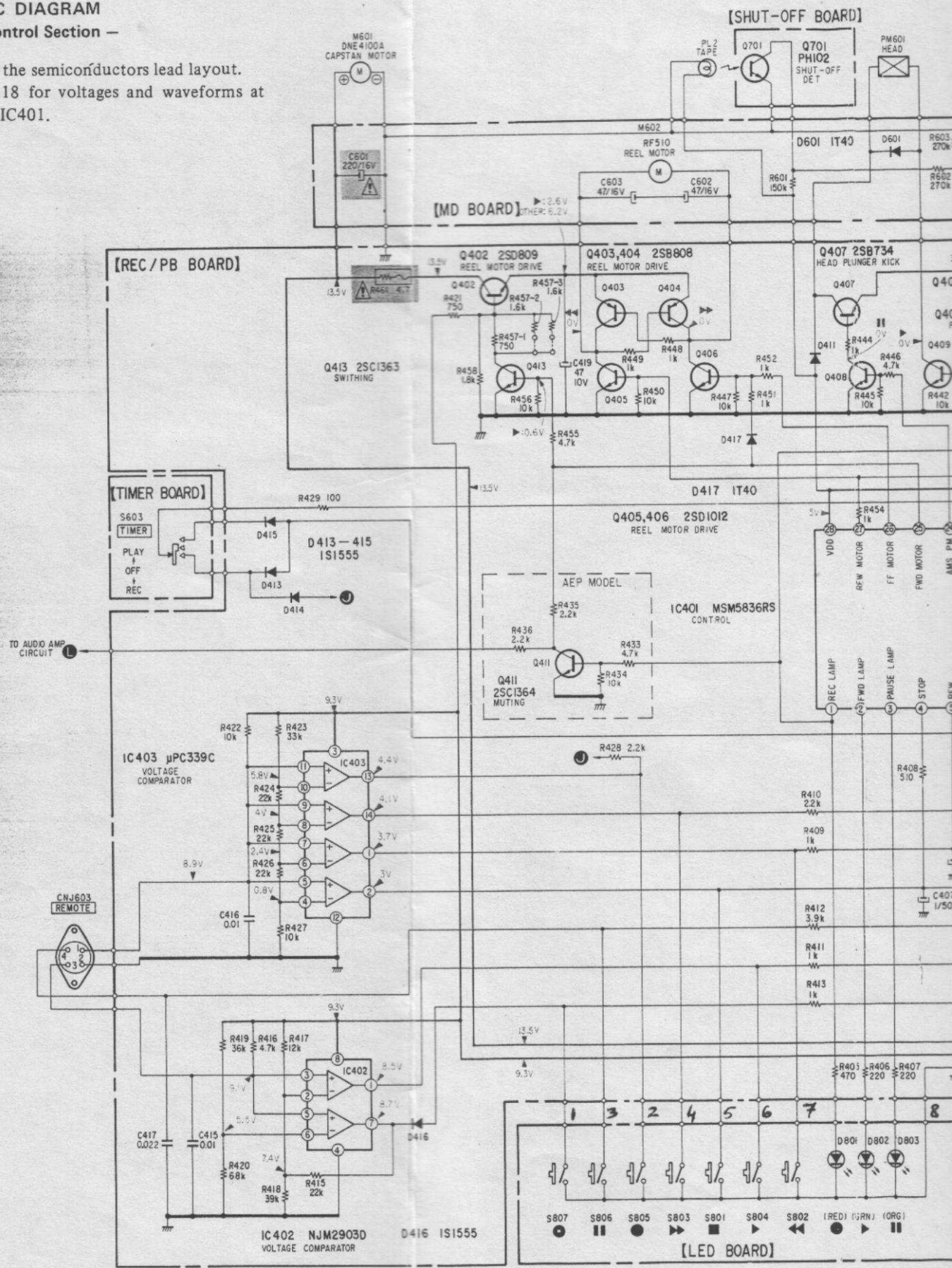
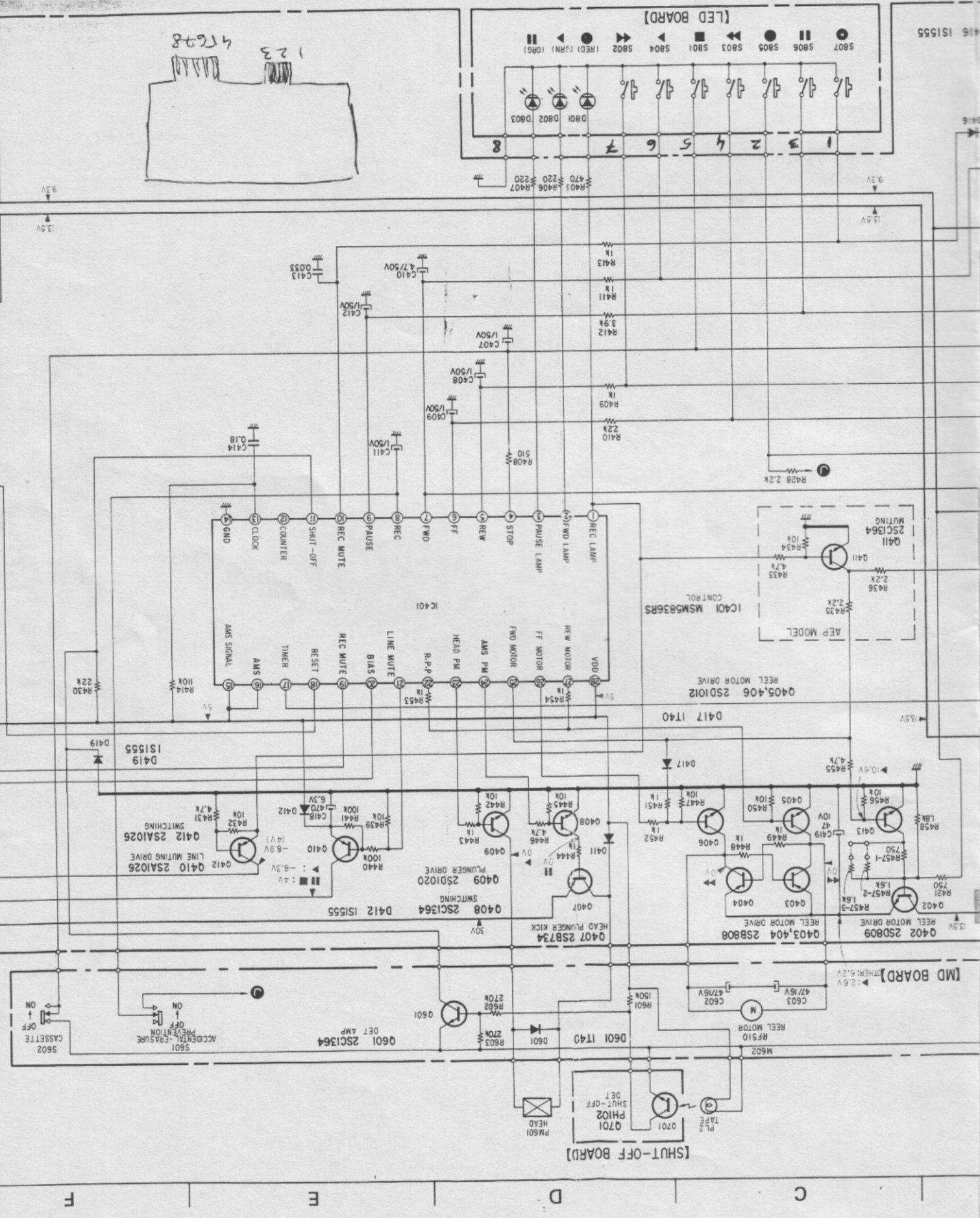


4-2. SCHEMATIC DIAGRAM
- System Control Section -

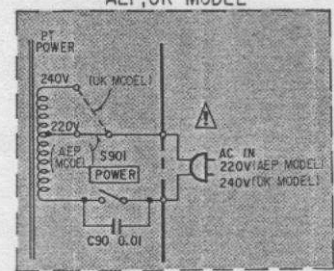
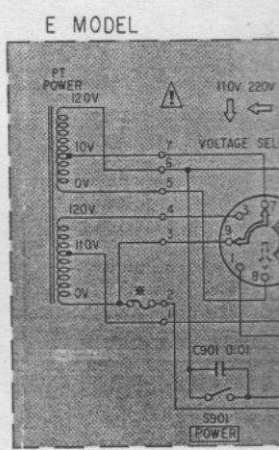
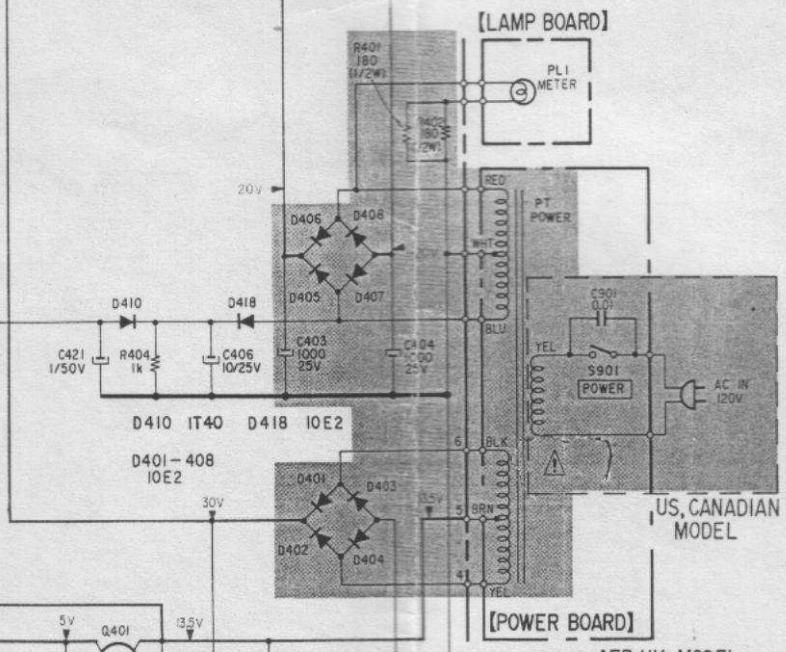
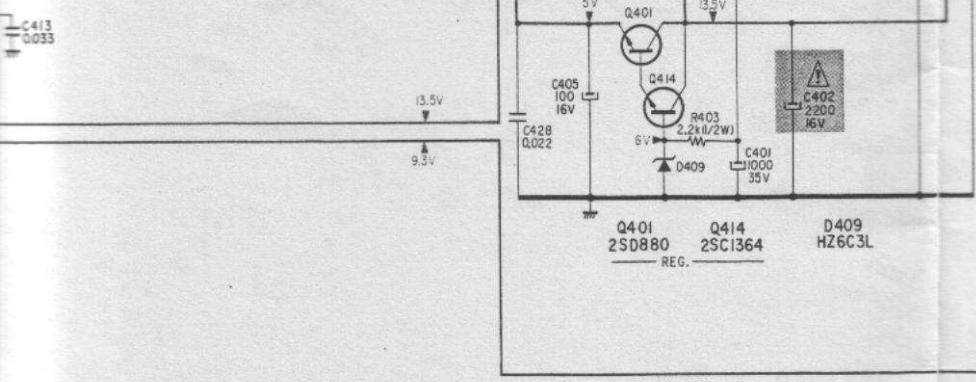
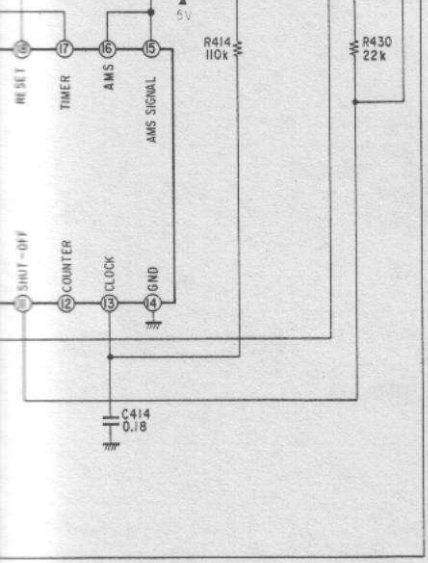
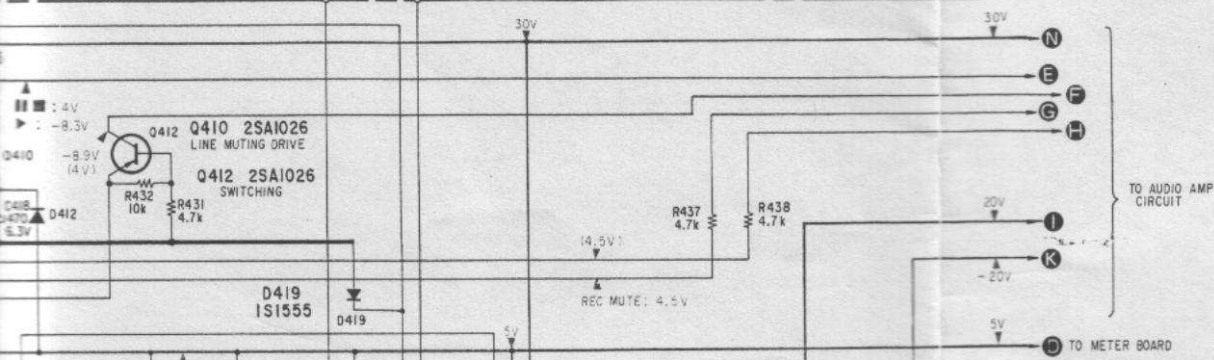
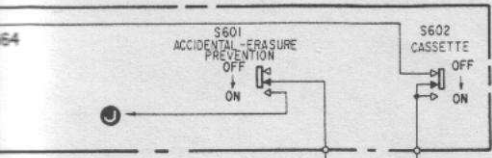
- See page 19 for the semiconductors lead layout.
- Refer to page 18 for voltages and waveforms at the terminal of IC401.

2
3
4
5





TC-FX4
 TC-FX4
 TC-FX4

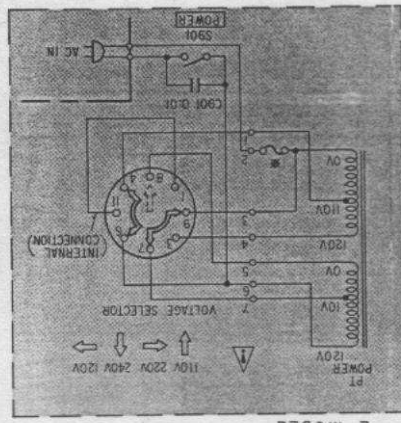


Note: Les composants identifiés par une trame et une marque ∇ sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

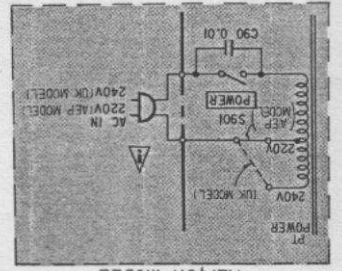
Note: The components identified by shading and mark ∇ are critical for safety. Replace only with part number specified.

Ref. No.	Switch	Position
S601	ACCIDENTAL ERASER PREVENTION	OFF
S602	CASSETTE TIMER	OFF
S801	STOP	OFF
S802	REW	OFF
S803	FF	OFF
S804	FWD	OFF
S805	REC	OFF
S806	PAUSE	OFF
S807	REC MUTE	OFF
S901	POWER	OFF

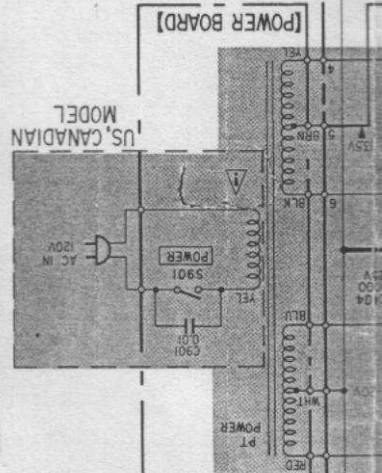
- Switches
- Voltage variations may be noted due to normal production tolerances.
- All capacitors are in μF unless otherwise noted. pF : pF
- 50WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in ohms, $\frac{1}{2}$ W unless otherwise noted. k Ω : 1000 Ω , M Ω : 1000 k Ω
- — : B+ bus.
- - - - : B- bus.
- Voltages are dc with respect to ground unless otherwise noted.
- Readings are taken under no-signal conditions with a VOM (20 k Ω /V).
- no mark: STOP
- \blacksquare : STOP
- \blacktriangledown : FWD
- \blacktriangleleft : REW
- \blacktriangleright : FF
- \bullet : REC
- \blacksquare : PAUSE



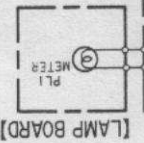
E MODEL



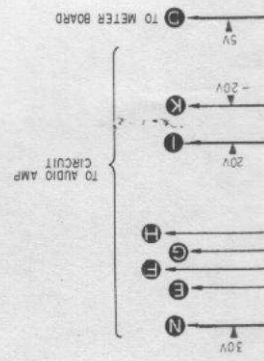
AEP, UK MODEL



US, CANADIAN MODEL



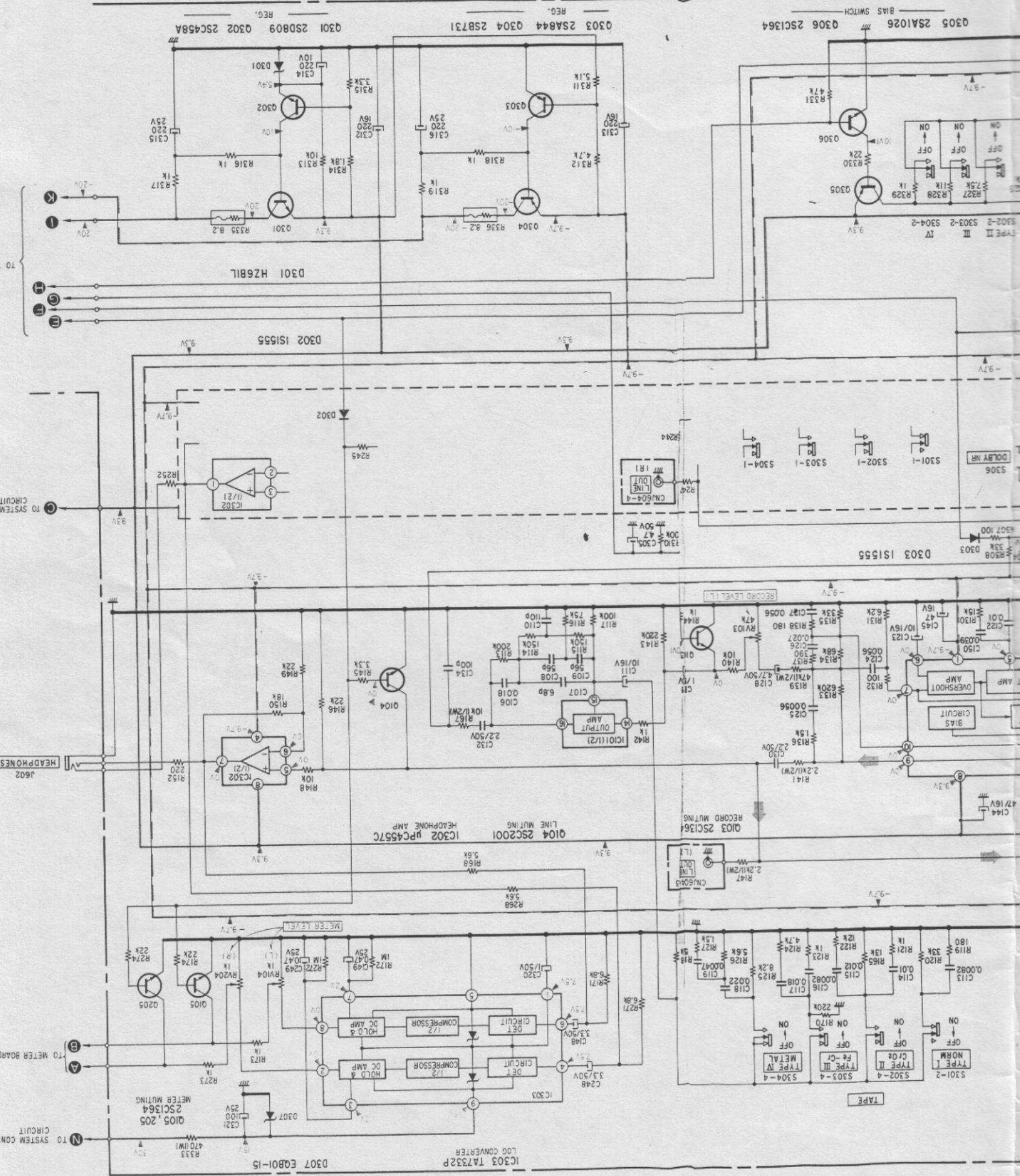
[LAMP BOARD]



TO METER BOARD

TO AUDIO AMP

TO AUDIO AMP



G

F

E

D

TC-FX4 TC-FX4

X4